

It consists of integrated dies of NTT Electronics' ExaSPEED 400-R DSP and Silicon photonics based Coherent Optical Sub-Assembly (COSA 2.0) in ...

Coherent (NYSE: COHR) will demonstrate multiple co-packaged optics (CPO) technologies at OFC 2026 in Los Angeles on March 17, 2026. Presentations include a 6.4T ...

Easier to scale up for higher performance and capacity by integrating more functions on a single chip.

Discover the evolution from 400G to 800G and 1.6T optical modules. Learn key technologies, CPO vs pluggable, and upgrade strategies for future-ready data centers.

A powerful showcase of silicon photonics, VCSEL, and InP-on-silicon technologies operating within a co-packaged optics architecture - advancing energy-efficient scaling for AI fabrics.

NTT Electronics starts shipping 400G coherent co-package device (CPD) samples implemented with integration of 64Gbaud Digital Signal ...

It consists of integrated dies of NTT Electronics' ExaSPEED 400-R DSP and Silicon photonics based Coherent Optical Sub-Assembly (COSA 2.0) in a single package, which can be ...

The company presented advancements across co-packaged optics (CPO), pluggable transceivers, 400G-per-lane optical links, optical circuit switching and multi-rail transport, as well as thermal ...

Partnering with Pilot Photonics, the project leverages chip-scale frequency comb technology to achieve unprecedented performance in linewidth, intensity noise and wavelength stability.

Silicon photonics is now a well-established technology and market for optical transceivers. In 2021, more than 9 million silicon photonic transceivers were shipped for datacenters.

The InP-based demonstration features a 400G-per-lane InP modulator array, illustrating a pathway toward higher lane speeds and the scalability required for future CPO architectures.

NTT Electronics starts shipping 400G coherent co-package device (CPD) samples implemented with integration of 64Gbaud Digital Signal Processor (DSP) die and silicon photonics ...

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