

There are different types of enterprise switches that perform various roles in these layer-based or hierarchical ethernet networks. This white paper introduces the following three types of network ...

Core switches provide the default gateway to downstream data center hosts. An active gateway IP and MAC address are configured for each VLAN to allow both core switches to represent ...

We're going to install two 48 port SFP+ switches at a co-location facility that will house all of our primary servers and storage. Both x690 switches have the core licence and will do layer 3. Our ...

This document describes the interoperation between H3C switches and third-party devices, as well as configuration of the associated parameters for interoperation.

The QM8700 switch runs the same MLNX-OS[®] software package as Mellanox FDR and EDR products to deliver complete chassis management of the firmware, power supplies, fans and ...

The cores will only route to other access switches and shouldn't encounter issues with spanning tree if configured with routed ports instead of switch ports. If you configure OSPF or EIGRP, they will route ...

In a Two-Tier architecture, each ToR access switch is connected to both core switches using MC-LAG to provide link load-balancing and fault tolerance. Redundant top-of-rack pairs using ...

As you add more switches to a floor, you must keep in mind the distribution of the uplinks across switches, and the impact on oversubscription during failure. For access points that are dual attached ...

This chapter describes how to set up a basic dual-core topology with an MDS 9000 switch configured for interop mode 1 and a McData 6064 switch. Devices are connected to both core switches and all ...

CS8500 switches create the highest scalability for large data aggregation, with the highest application performance of complex computations while data moves through the data center network.

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